## **APPLICATION DATA SHEET**

Electronic Version 0.0.11

Stylesheet Version: 1.0

Attorney Docket Number: PS-70

Application Type:

utility

Title of Invention:

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the state

ESD-Isolation Circuit Driving Gate of Bus-Switch Transistor During

ESD Pulse Between Two I/O Pins

## Reguest Not To Publish

I hereby request that the attached application not be published under 35 U.S.C. 122(b). I hereby certify that the invention disclosed in the attached application has not and will not be the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing.

**Customer Number Attorney:** 

23933



Customer Number Correspondence Address:

23933



Assignee (Publish): Pericom Semiconductor Corp.

## INVENTOR(s):

Primary Citizenship:

USA

Given Name:

David

Family Name:

Kwong

Residence City:

Fremont

Residence State:

CA US

Residence Country: Address:

44071 Linda Vista Rd.

Fremont CA, 94539 US

Primary Citizenship:

**USA** 

Given Name:

Paul

Middle Name:

C.F.

Family Name:

Tong

Residence City:

San Jose

Residence State:

CA

Residence Country:

US

Address:

1545 Valley Crest Drive

San Jose CA, 95131 US